

### **Ordering Information**

Part Number		Junction Temperature			Deekere	
Standard	RoHS Compliant <sup>(2)</sup>	Range <sup>(1)</sup>	Voltage	Current	Package	
HG29300-3.3BU	HG29300-3.3WU <sup>(2)</sup>	-40°C to +125°C	3.3	3.0A	3-Pin TO-263	
HG29300-5.0BT	HG29300-5.0WT <sup>(2)</sup>	-40°C to +125°C	5	3.0A	3-Pin TO-220	
HG29300-5.0BU	HG29300-5.0WU <sup>(2)</sup>	-40°C to +125°C	5	3.0A	3-Pin TO-263	
HG29300-12BT	HG29300-12WT <sup>(2)</sup>	-40°C to +125°C	12	3.0A	3-Pin TO-220	
HG29300-12BU	HG29300-12WU <sup>(2)</sup>	-40°C to +125°C	12	3.0A	3-Pin TO-263	
HG29301-3.3BT	HG29301-3.3WT <sup>(2)</sup>	-40°C to +125°C	3.3	3.0A	5-Pin TO-220	
HG29301-3.3BU	HG29301-3.3WU <sup>(2)</sup>	-40°C to +125°C	3.3	3.0A	5-Pin TO-263	
HG29301-5.0BT	HG29301-5.0WT <sup>(2)</sup>	-40°C to +125°C	5	3.0A	5-Pin TO-220	
HG29301-5.0BU	HG29301-5.0WU <sup>(2)</sup>	-40°C to +125°C	5	3.0A	5-Pin TO-263	
HG29301-12BT	HG29301-12WT <sup>(2)</sup>	-40°C to +125°C	12	3.0A	5-Pin TO-220	
HG29301-12BU	HG29301-12WU <sup>(2)</sup>	-40°C to +125°C	12	3.0A	5-Pin TO-263	
HG29302BT	HG29302WT <sup>(2)</sup>	-40°C to +125°C	Adjustable	3.0A	5-Pin TO-220	
HG29302BU	HG29302WU <sup>(2)</sup>	-40°C to +125°C	Adjustable	3.0A	5-Pin TO-263	
HG29303BT	HG29303WT <sup>(2)</sup>	-40°C to +125°C	Adjustable	3.0A	5-Pin TO-220	
HG29303BU	HG29303WU <sup>(2)</sup>	-40°C to +125°C	Adjustable	3.0A	5-Pin TO-263	

Notes:

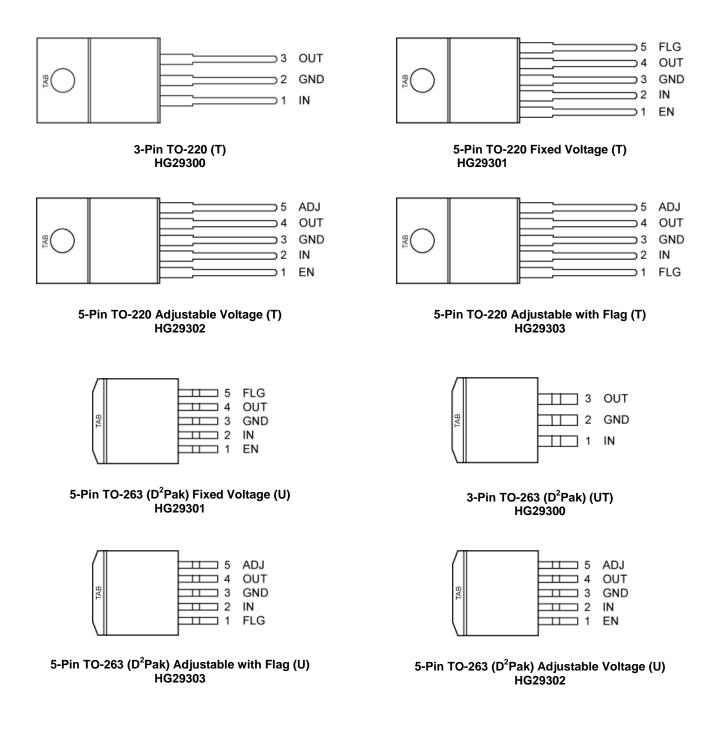
1. Junction temperature.

2. RoHS compliant with 'high-melting solder' exemption.

3. Special Order; please contact factory for availability.



## **Pin Configuration**





# **Pin Description**

Pin Number TO-220 TO-263	Pin Name
1	INPUT: Supplies the current to the output power device.
2	GND: TAB is also connected internally to the IC's ground on D-PAK.
3	OUTPUT: The regulator output voltage.

## **Pin Description**

Pin Number Fixed TO-220 TO-263	Pin Number Adjustable T O - 2 2 0 TO-263	Pin Number Adj. with Flag TO-220 TO-263	Pin Name
1	1	_	ENABLE: CMOS compatible control input. Logic high = enable, logic low = shutdown.
2	2	2	INPUT: Supplies the current to the output power device
3, TAB	3, TAB	3, TAB	GND: TAB is also connected internally to the IC's ground on D-PAK.
4	4	4	OUTPUT: The regulator output voltage
_	5	5	ADJUST: Adjustable regulator feedback input that connects to the resistor voltage divider that is placed from OUTPUT to GND in order to set the output voltage.
5		1	FLAG: Active low error flag output signal that indicates an output fault condition



### Absolute Maximum Ratings<sup>(1)</sup>

Input Supply Voltage (V <sub>IN</sub> ) <sup>(1)</sup>	–20V to +60V
Enable Input Voltage (V <sub>EN</sub> )	
Lead Temperature (soldering, 5sec.)	
Power Dissipation	Internally Limited
Storage Temperature Range	65°C to +150°C
ESD Rating.	Note 3

### **Operating Ratings**<sup>(2)</sup>

Operating Junction Temperature40°C to +12	25°C
Maximum Operating Input Voltage	26V
Package Thermal Resistance	
TO-220 (θ <sub>JC</sub> )	C/W
TO-263 (θ <sub>JC</sub> )	C/W

### Electrical Characteristics<sup>(4, 11)</sup>

 $V_{IN} = V_{OUT} + 1V$ ;  $I_{OUT} = 10$ mA;  $T_J = 25$ °C. **Bold** values indicate -40°C  $\leq T_J \leq +125$ °C, unless noted.

Parameter	Condition	Min.	Тур.	Max.	Units
	I <sub>OUT</sub> = 10mA	-1		1	%
Output Voltage	$10mA \le I_{OUT} \le I_{FL}$ , $(V_{OUT} + 1V) \le V_{IN} \le 26V$	-2		2	70
Line Regulation	$I_{OUT} = 10mA$ , $(V_{OUT} + 1V) \le V_{IN} \le 26V$		0.06	0.5	%
Load Regulation	$V_{IN} = V_{OUT} + 1V$ , $10mA \le I_{OUT} \le 1.5A$ <sup>(8)</sup>		0.2	1	%
$\frac{\Delta V}{\Delta T}$ o	Output Voltage <sup>(8)</sup> Temperature Coefficient.		20	100	ppm/℃
Dropout Voltage	$ \begin{array}{ll} \Delta V_{OUT} = -1\%^{(6)} & & \\ HG29300 & I_{OUT} \\ = 100 \text{mA} & I_{OUT} = 1.5 \text{A} \\ & I_{OUT} = 3 \text{A} I_{OUT} \end{array} $		80 250 370	175 600	mV

#### Notes:

- 1. Maximum positive supply voltage of 60V must be of limited duration (<100msec) and duty cycle (≤1%). The maximum continuous supply voltage is 26V. Exceeding the absolute maximum rating may damage the device.
- 2. The device is not guaranteed to function outside its operating rating.
- 3. Devices are ESD sensitive. Handling precautions recommended.
- 4. Specification for packaged product only.
- 5. Dropout voltage is defined as the input-to-output differential when the output voltage drops to 99% of its normal value with V<sub>OUT</sub> + 1V applied to V<sub>IN</sub>.
- V<sub>IN</sub> = V<sub>OUT (nominal)</sub> + 1V. For example, use V<sub>IN</sub> = 4.3V for a 3.3V regulator or use 6V for a 5V regulator. Employ pulse-testing procedures to pin current.
- 7. Ground pin current is the regulator quiescent current. The total current drawn from the source is the sum of the load current plus the ground pin current.
- 8. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 9. Thermal regulation is defined as the change in output voltage at a time T after a change in power dissipation is applied, excluding load or line regulation effects. Specifications are for a200mA load pulse at VIN = 20V (a 4W pulse) for T = 10ms.
- 10. Comparator thresholds are expressed in terms of a voltage differential at the Adjust terminal below the nominal reference voltage measured at 6V input. To express these thresholds in terms of output voltage change, multiply by the error amplifier gain =  $V_{OUT}/V_{REF}$  = (R1 + R2)/R2. For example, at a programmed output voltage of 5V, the Error output is guaranteed to go low when the output drops by 95mV x 5V/1.240V = 384mV. Thresholds remain constant as a percent of  $V_{OUT}$  is varied, with the dropout warning occurring at typically 5% below nominal, 7.7% guaranteed.
- 11.  $V_{EN} \le 0.8V$  and  $V_{IN} \le 26V$ ,  $V_{OUT} = 0$ .
- 12. When used in dual supply systems where the regulator load is returned to a negative supply, the output voltage must be diode clamped to ground.



# Electrical Characteristics<sup>(4, 12)</sup> (Continued)

 $V_{IN} = V_{OUT} + 1V$ ;  $I_{OUT} = 10$ mA;  $T_J = 25^{\circ}$ C. **Bold** values indicate  $-40^{\circ}$ C  $\leq T_J \leq +125^{\circ}$ C, unless noted.

Parameter	Condition		Min.	Тур.	Max.	Units
Ground Current	HG29300 Note 8	I <sub>OUT</sub> = 1.5A, V <sub>IN</sub> = V <sub>OUT</sub> + 1V I <sub>OUT</sub> = 3A		10 37	35	mA
I <sub>GRNDDO</sub> Ground Pin Current at Dropout	V <sub>IN</sub> = 0.5V less than HG29300	specified V <sub>OUT</sub> × I <sub>OUT</sub> = 10mA		1.7		mA
Current Limit	HG29300	V <sub>OUT</sub> = 0V <sup>(7)</sup>		4.5	5.0	A
e <sub>n</sub> , Output Noise Voltage (10Hz to 100kHz) I <sub>L</sub> = 100mA	$C_L = 10\mu F$ $C_L = 33\mu F$			400 260		μV (rms)

Reference – HG29302/29	303				
Reference Voltage		1.228 <b>1.215</b>	1.240	1.252 <b>1.265</b>	V
Reference Voltage		1.203		1.277	V
Adjust Pin Bias Current			40	80 <b>120</b>	nA
Reference Voltage Temperature Coefficient	Note 9		20		ppm/⁰C
Adjust Pin Bias Current Temperature Coefficient			0.1		nA/°C
Flag Output (Error Comp	arator) – HG29301/29303				
Output Leakage Current	V <sub>OH</sub> = 26V		0.01	1.00 <b>2.00</b>	μΑ
Output Low Voltage	Device set for 5V, $V_{IN} = 4.5V$ $I_{OL} = 250 \mu A$		220	300 <b>400</b>	mV
Upper Threshold Voltage	Device set for 5V (10)	40 <b>25</b>	60		mV
Lower Threshold Voltage	Device set for 5V (10)		75	95 <b>140</b>	mV
Hysteresis	Device set for 5V <sup>(10)</sup>		15		mV



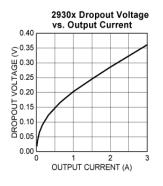
# Electrical Characteristics<sup>(4, 12)</sup> (Continued)

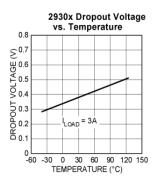
 $V_{IN} = V_{OUT} + 1V$ ;  $I_{OUT} = 10$ mA;  $T_J = 25^{\circ}$ C. **Bold** values indicate  $-40^{\circ}$ C  $\leq T_J \leq +125^{\circ}$ C, unless noted.

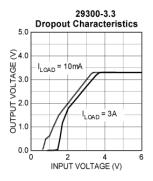
Parameter	Condition	Min.	Тур.	Max.	Units
ENABLE Input – HG2930	1/29302				
Input Logic Voltage Low (OFF) High (ON)		2.4		0.8	V
Enable Pin	V <sub>EN</sub> = 26V		100	600 <b>750</b>	
Input Current	V <sub>EN</sub> = 0.8V	0.7		2 <b>4</b>	μA
Regulator Output Current in Shutdown	Note 11		10	500	μA

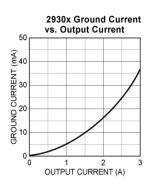


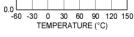
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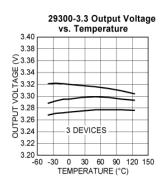


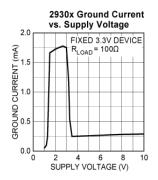


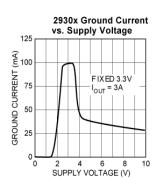




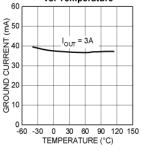


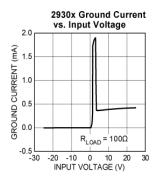




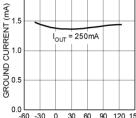


2930x Ground Current vs. Temperature



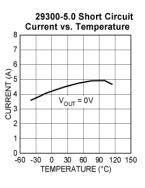


2930x Ground Current vs. Temperature



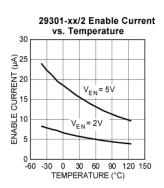
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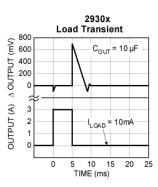
0.0 ..., -60 -30 0 30 60 90 120 150 TEMPERATURE (°C)

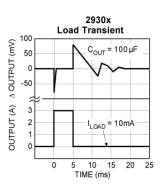


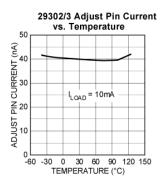


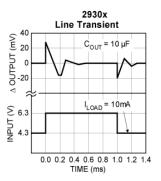
## Typical Characteristics (HG2930x) (Continued)

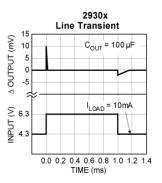


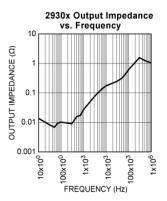






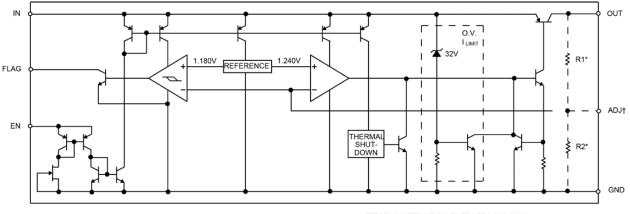








## **Functional Diagram**



\* FEEDBACK NETWORK IN FIXED VERSIONS ONLY † ADJUSTABLE VERSION ONLY





### **Application Information**

The HG29300 are high-performance low-dropout voltage regulators suitable for all moderate to high-current voltage regulator applications. Their 350 mV to 425 mV typical dropout voltage at full load make them especially valuable in battery powered systems and as high efficiency noise filters in "post-regulator" applications. Unlike older NPN-pass transistor designs, where the minimum dropout voltage is limited by the base-emitter voltage drop and collector-emitter saturation voltage, dropout performance of the PNP output of these devices is limited merely by the low V<sub>CE</sub> saturation voltage.

A trade-off for the low-dropout voltage is a varying base driver requirement. But Micrel's Super ßeta PNP<sup>®</sup> process reduces this drive requirement to merely 1% of the load current.

The HG29300 family of regulators are fully protected from damage due to fault conditions. Current limiting is provided. This limiting is linear; output current under overload conditions is constant. Thermal shutdown disables the device when the die temperature exceeds the 125°C maximum safe operating temperature. Line transient protection allows device (and load) survival even when the input voltage spikes between -20V and +60V. When the input voltage exceeds approximately 32V, the over voltage sensor disables the regulator. The output structure of these regulators allows voltages in excess of the desired output voltage to be applied without reverse current flow. HG29301 and HG29302 versions offer a logic level ON/OFF control: when disabled, the devices draw nearly zero current. An additional feature of this regulator family is a common pinout: a design's current requirement may change up or down yet use the same board layout, as all of these regulators have identical pinouts.

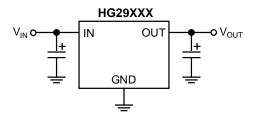


Figure 3. Linear Regulators Require Only Two Capacitors for Operation

### Thermal Design

Linear regulators are simple to use. The most complicated design parameters to consider are thermal characteristics. Thermal design requires the following application-specific parameters:

- Maximum ambient temperature, T<sub>A</sub>
- Output Current, I<sub>OUT</sub>
- Output Voltage, V<sub>OUT</sub>
- Input Voltage, V<sub>IN</sub>

First, we calculate the power dissipation of the regulator from these numbers and the device parameters from this datasheet.

$$P_{D} = I_{OUT} (1.01 V_{IN} - V_{OUT})$$
 Eq. 1

where the ground current is approximated by 1% of  $I_{OUT}$ . Then the heat sink thermal resistance is determined with Equation 2:

$$\theta_{SA} = \frac{T_{JMAX} - T_A}{P_D} - (\theta_{JC} + \theta_{CS})$$
 Eq. 2

where  $T_{JMAX} \le 125^{\circ}C$  and  $\theta_{CS}$  is between 0 and 2°C/W.

The heat sink may be significantly reduced in applications where the minimum input voltage is known and is large compared with the dropout voltage. Use a series input resistor to drop excessive voltage and distribute the heat between this resistor and the regulator. The low-dropout properties of Micrel Super ßeta PNP<sup>®</sup> regulators allow very significant reductions in regulator power dissipation and the associated heat sink without compromising performance. When this technique is employed, a capacitor of at least 0.1µF is needed directly between the input and regulator ground.

Please refer to Application Note 8 and Application Hint 17 for further details and examples on thermal design and heat sink specification.



For example, given an expected maximum ambient temperature (T<sub>A</sub>) of 75°C with  $V_{IN} = 3.3V$ ,  $V_{OUT} = 2.5V$ , and  $I_{OUT} = 1.5A$ , first calculate the expected P<sub>D</sub> using Equation 3:

Next, calcualte the junction temperature for the expected power dissipation.

Now determine the maximum power dissipation allowed that would not exceed the IC's maximum junction temperature (125°C) without the useof a heat sink by:

$$\begin{split} \mathsf{P}_{\mathsf{D}(\mathsf{MAX})} &= (\mathsf{T}_{\mathsf{J}(\mathsf{MAX})} - \mathsf{T}_{\mathsf{A}}) / \theta_{\mathsf{JA}} \\ &= (125^{\circ} \mathsf{C} - 75^{\circ} \mathsf{C}) / (56^{\circ} \mathsf{C} / \mathsf{W}) = 0.893 \mathsf{W} \\ & \mathsf{Eq. 5} \end{split}$$

#### **Capacitor Requirements**

For stability and minimum output noise, a capacitor on the regulator output is necessary. The value of this capacitor is dependent upon the output current; lower currents allow smaller capacitors. The HG29300 regulators are stable with the following minimum capacitor values at full load, as noted in Table 1:

Device	Full Load Capacitor
HG29300	10µF

#### Table 1. Minimum Capacitor Values at Full Load

This capacitor need not be an expensive low ESR type: aluminum electrolytics are adequate. In fact, extremely low ESR capacitors may contribute to instability. Tantalum capacitors are recommended for systems where fast load transient response is important.

Where the regulator is powered from a source with high AC impedance, a  $0.1\mu$ F capacitor connected between Input and GND is recommended. This capacitor should have good characteristics to above 250kHz.

Device

	Minimum Load
HG29300	7mA

#### Table 2. Minimum Load Currents

#### Adjustable Regulator Design

The adjustable regulator versions, HG29302 and HG29303, allow programming the output voltage anywhere between 1.25V and the 25V. Two resistors are used. The resistor values are calculated by Equation 6:

$$R_1 = R_2 \times \left(\frac{V_{OUT}}{1.240} - 1\right)$$
 Eq. 6

where  $V_{OUT}$  is the desired output voltage. Figure 4 shows component definition. Applications with widely varying load currents may scale the resistors to draw the minimum load current required for proper operation (see "Minimum Load Current" sub-section).

### Error Flag

Eq. 4

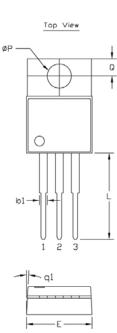
HG29301 and HG29303 versions feature an Error Flag, which looks at the output voltage and signals an error condition when this voltage drops 5% below its expected value. The error flag is an open-collector output that pulls low under fault conditions. It may sink 10mA. Low output voltage signifies a number of possible problems, including an overcurrent fault (the device is in current limit) and low input voltage. The flag output is inoperative during overtemperature shutdown conditions.

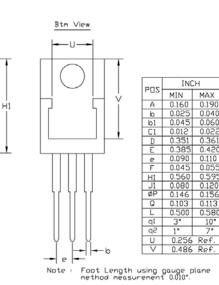
### Enable Input

HG29301 and HG29302 versions feature an enable (EN) input that allows ON/OFF control of the device. Special design allows "zero" current drain when the device is disabled—only microamperes of leakage current flows. The EN input has TTL/CMOS compatible thresholds for simple interfacing with logic, or may be directly tied to  $\leq$ 30V. Enabling the regulator requires approximately 20µA of current.



# Package Information<sup>(1)</sup>





3-Pin TO-220 (T)

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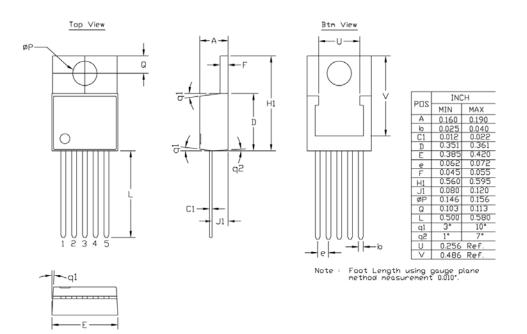
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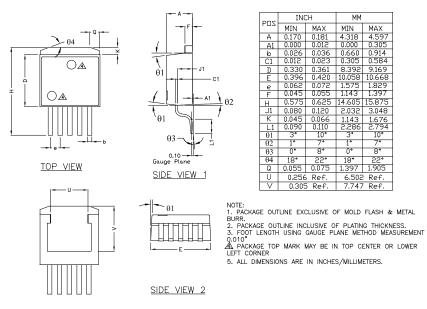
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5-Pin TO-220 (T)

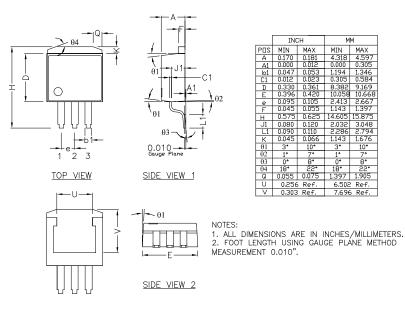


# Package Information<sup>(1)</sup> (Continued)



BOTTOM VIEW

5-Pin TO-263 (U)



BOTTOM VIEW

3-Pin TO-263 (U)